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N: \_\_\_\_\_

Inventor: Miller et al

Title: Methods and systems for a forming slots in a semiconductor substrate

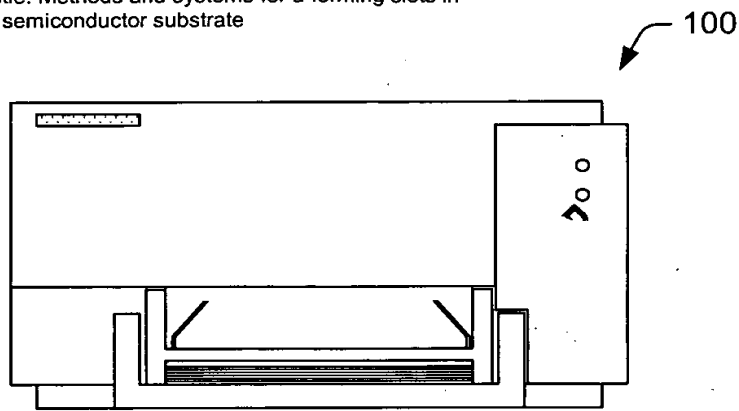


Fig.1

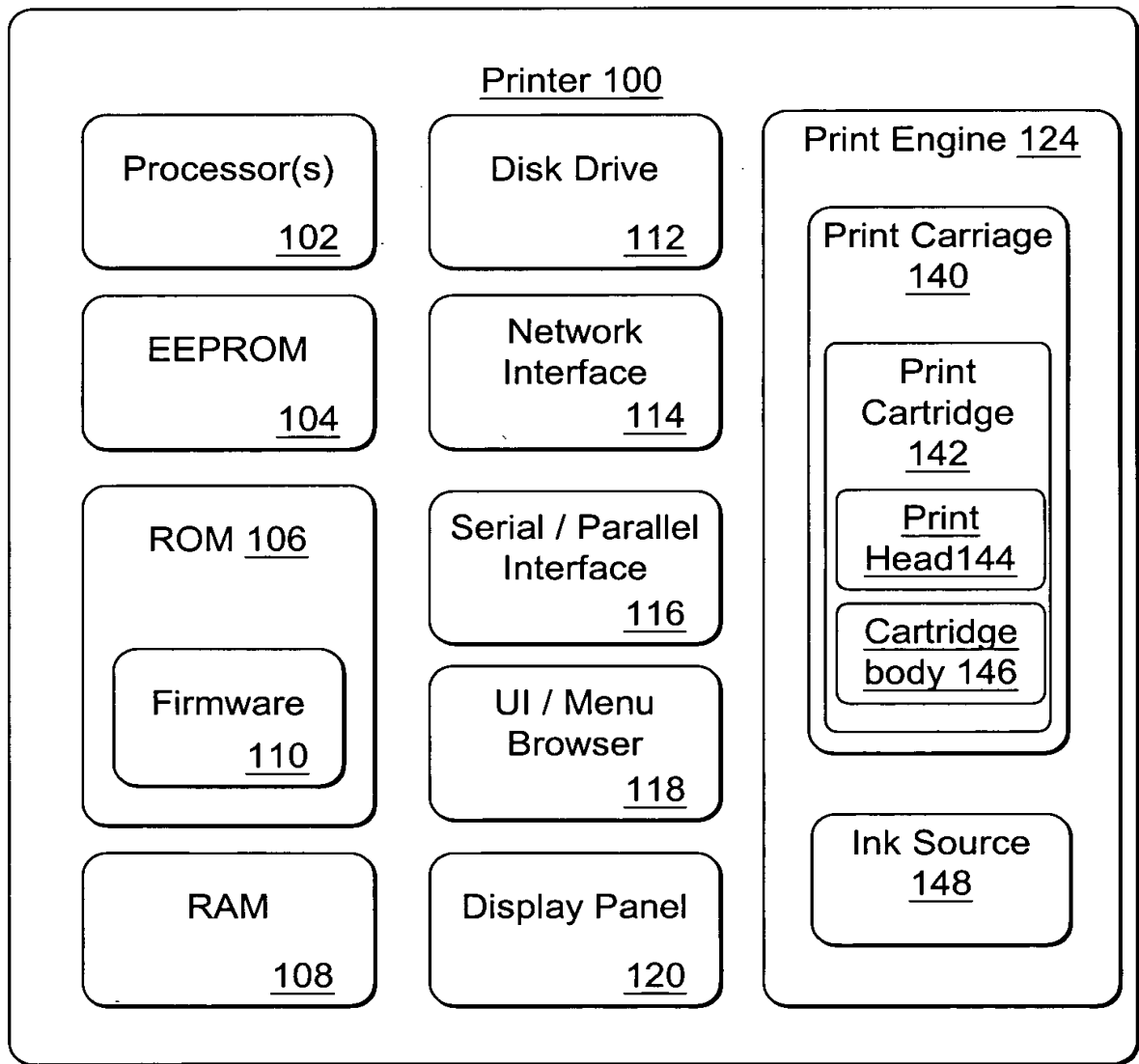


Fig. 2

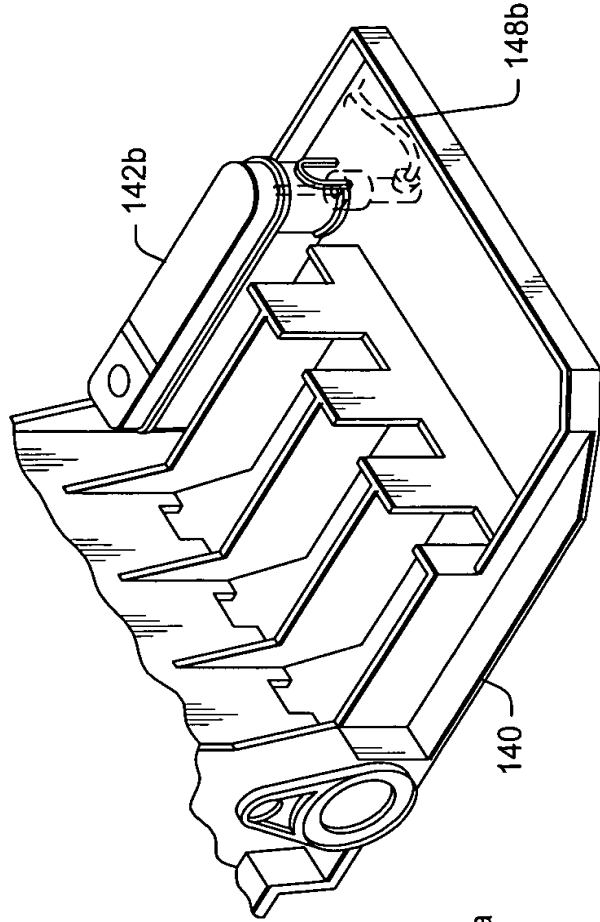


FIG. 3

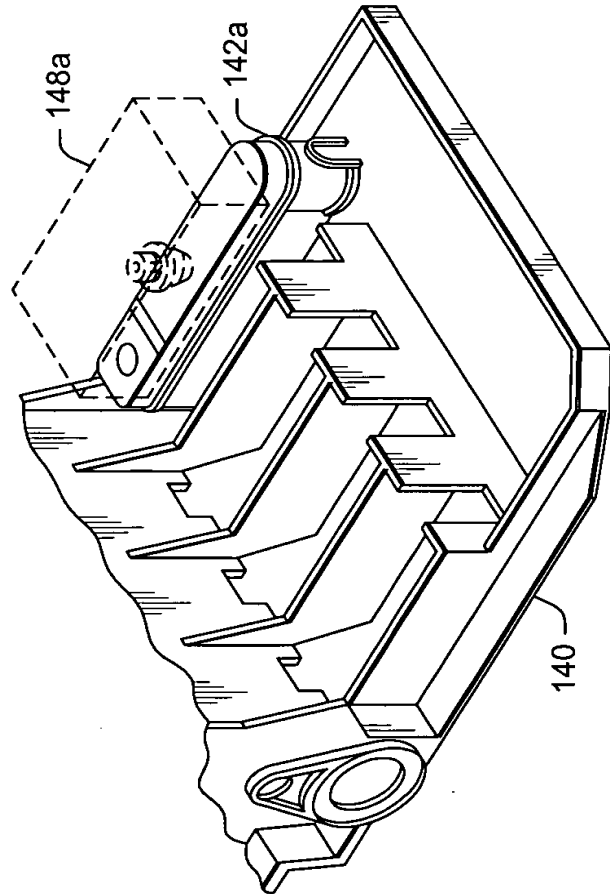


FIG. 4

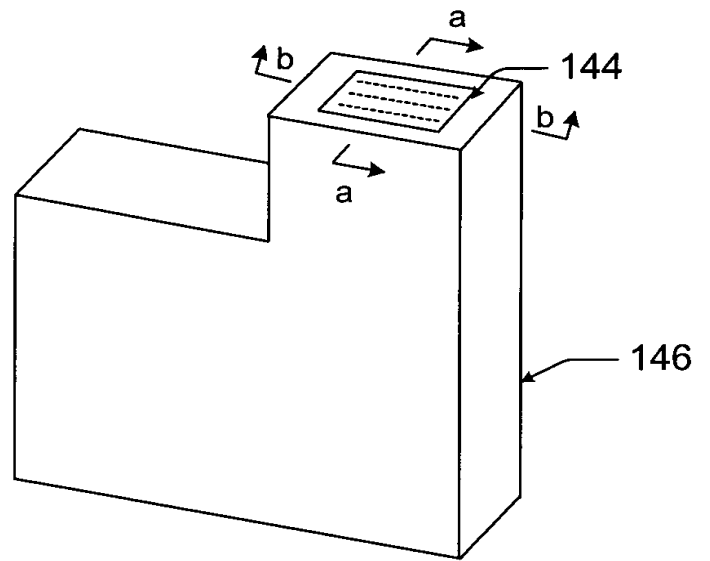


Fig. 5

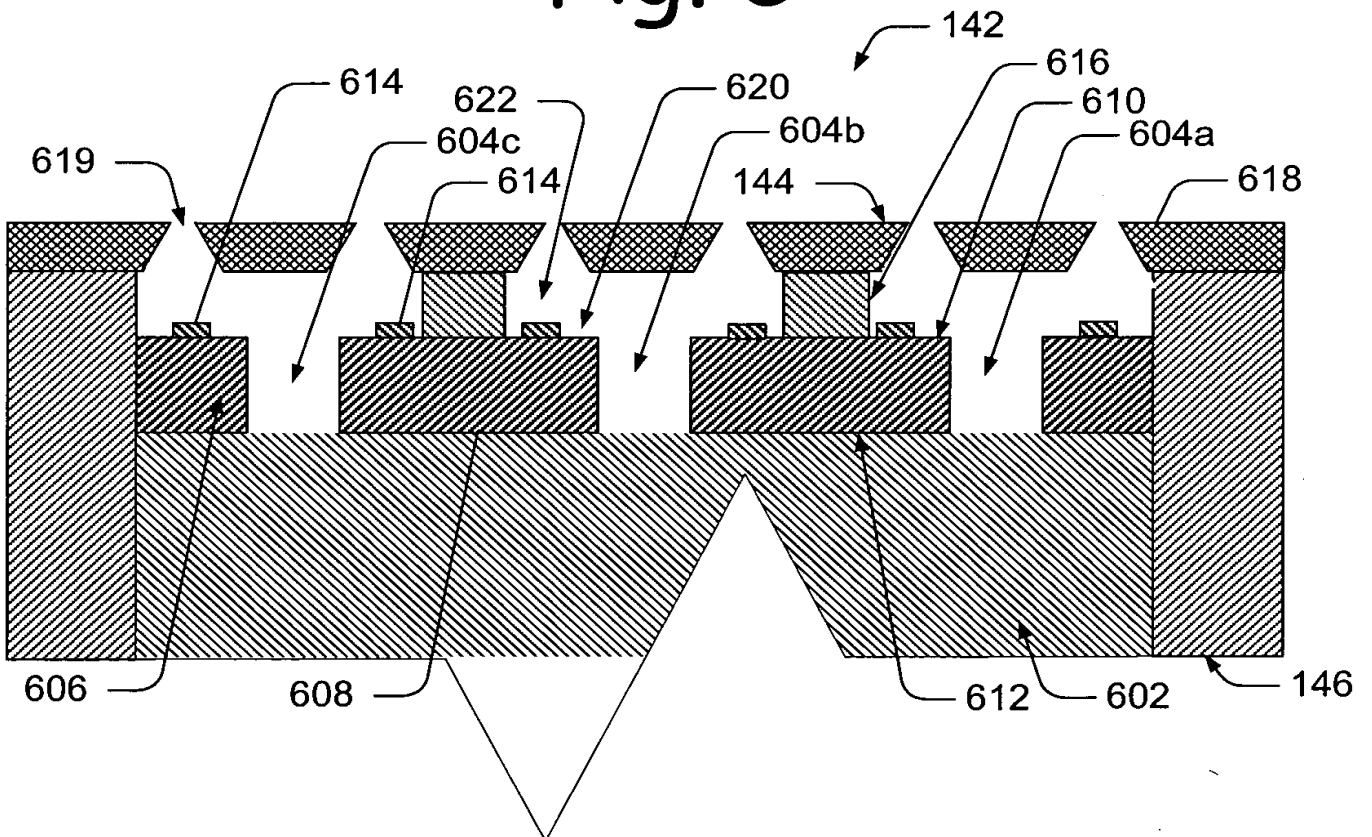


Fig. 6

N: \_\_\_\_\_

Inventor: Miller et al

Title: Methods and systems for a forming slots in  
a semiconductor substrate

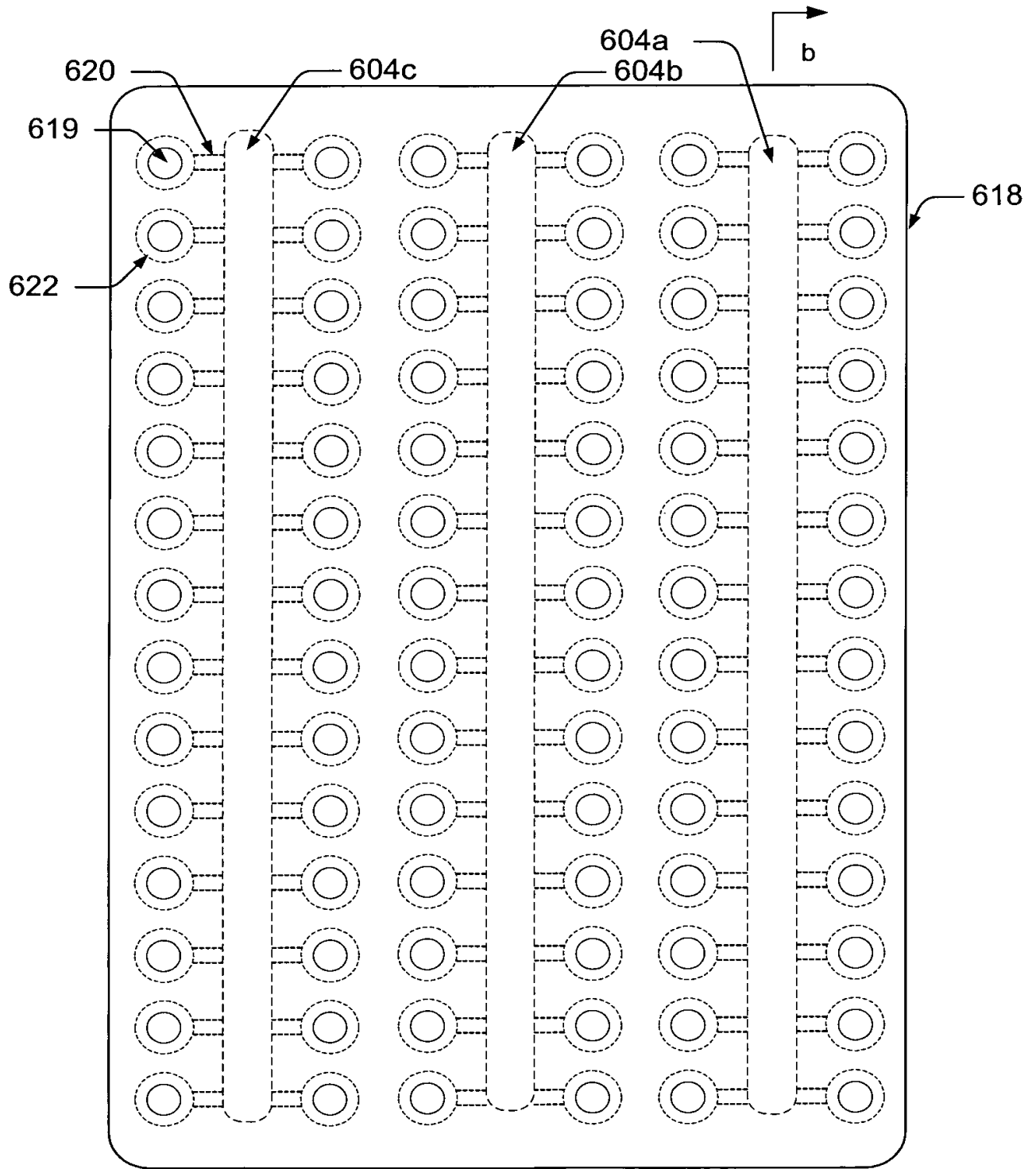


Fig. 7

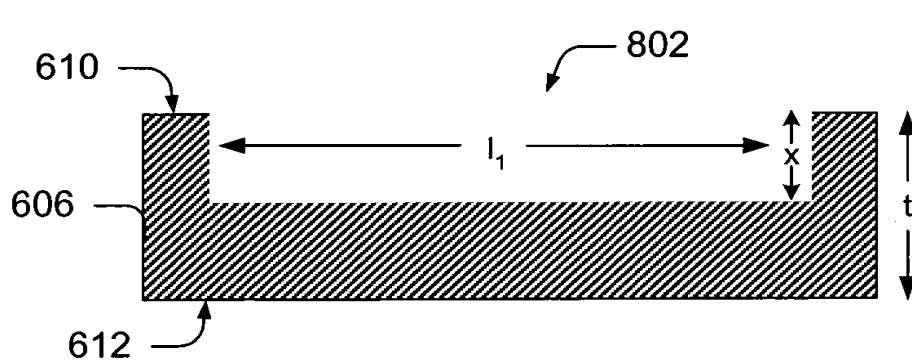


Fig. 8a

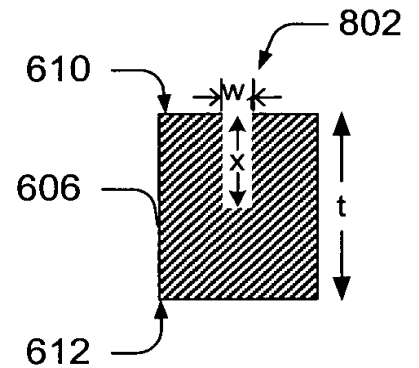


Fig. 8b

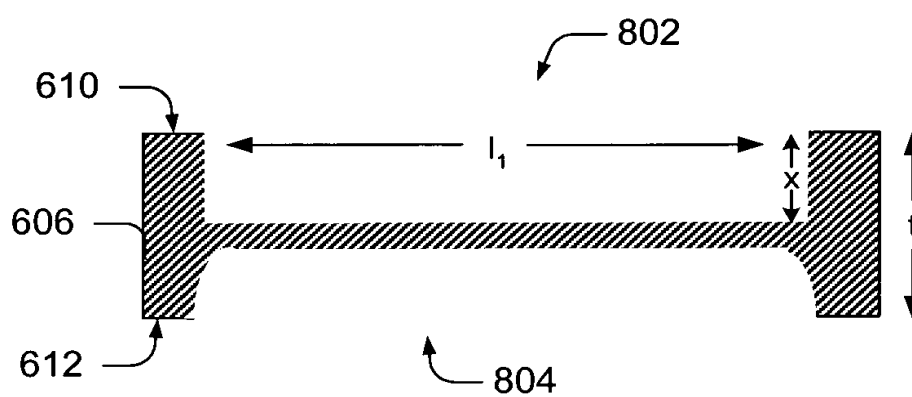


Fig. 8c

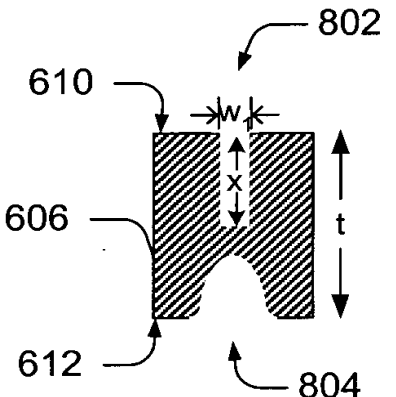


Fig. 8d

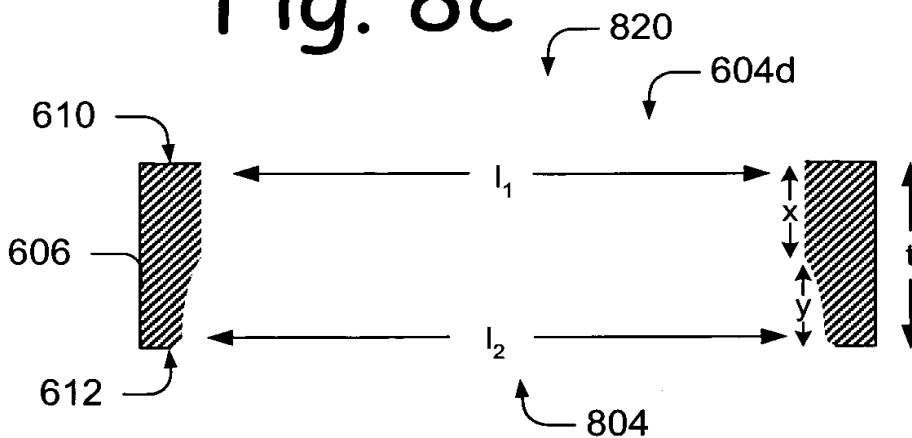


Fig. 8e

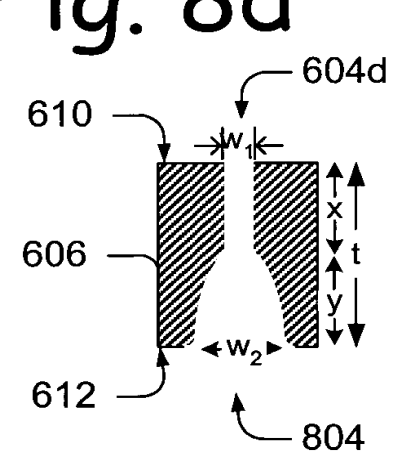


Fig. 8f

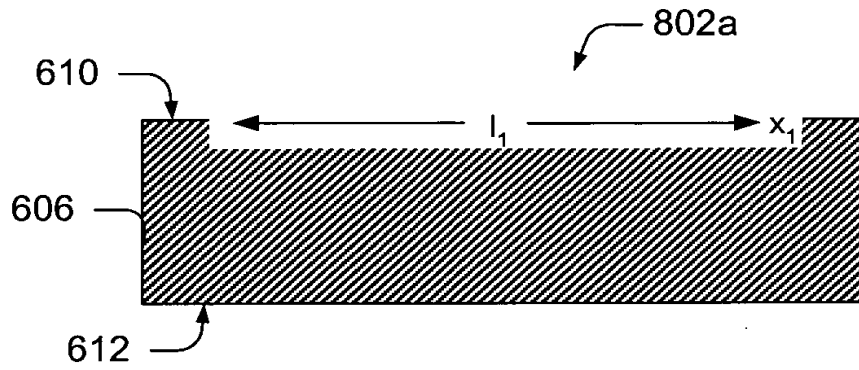


Fig. 9a

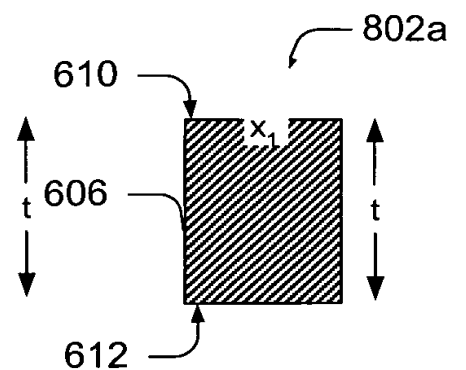


Fig. 9b

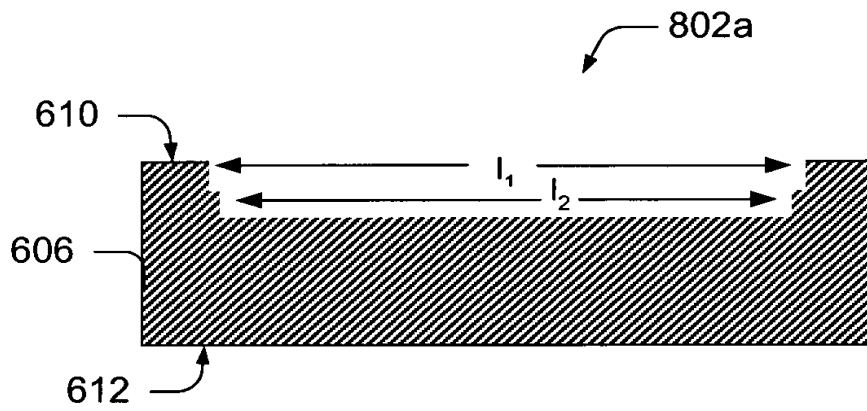


Fig. 9c

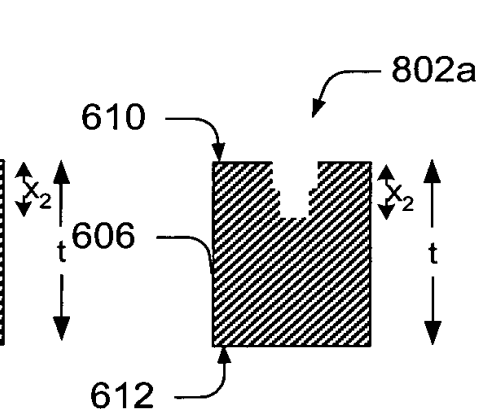


Fig. 9d

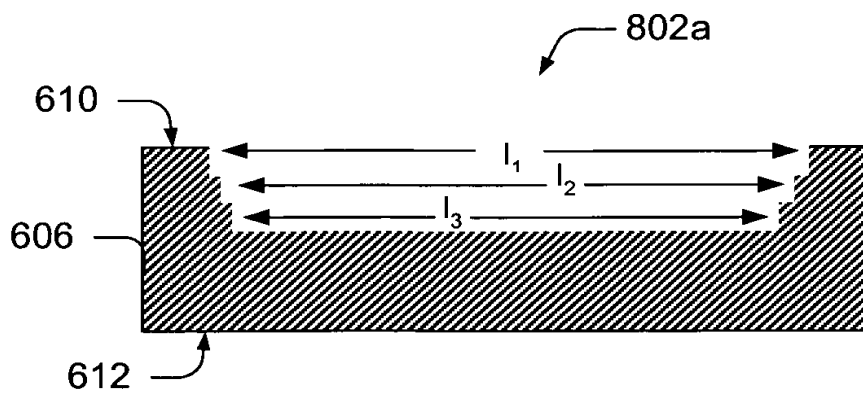


Fig. 9e

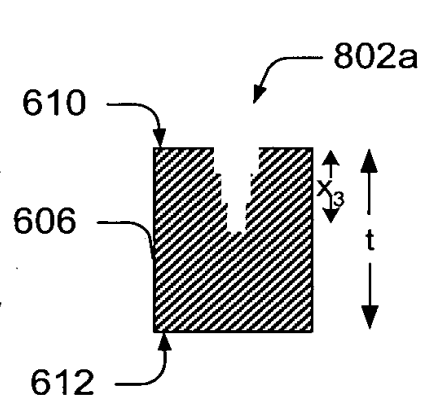


Fig. 9f

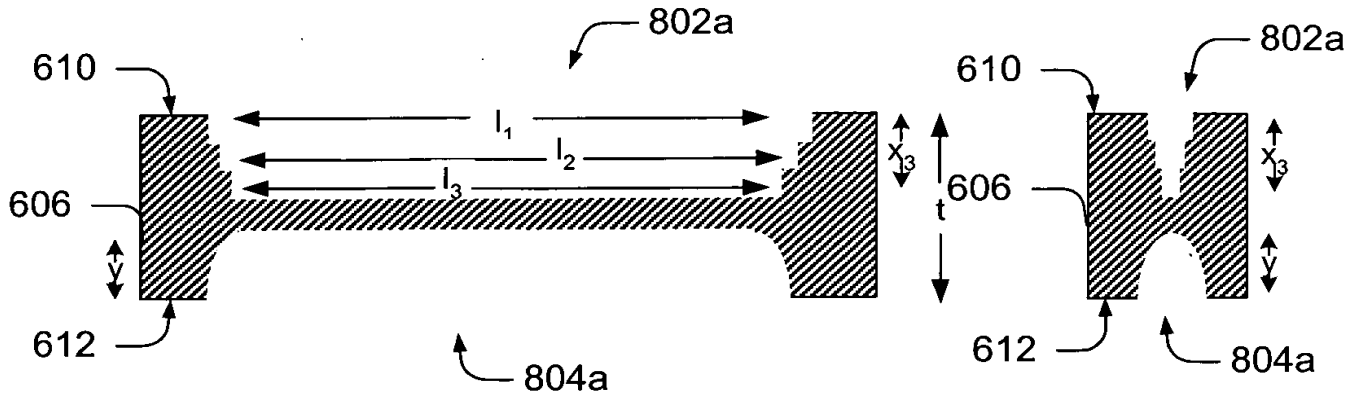


Fig. 9g

Fig. 9h

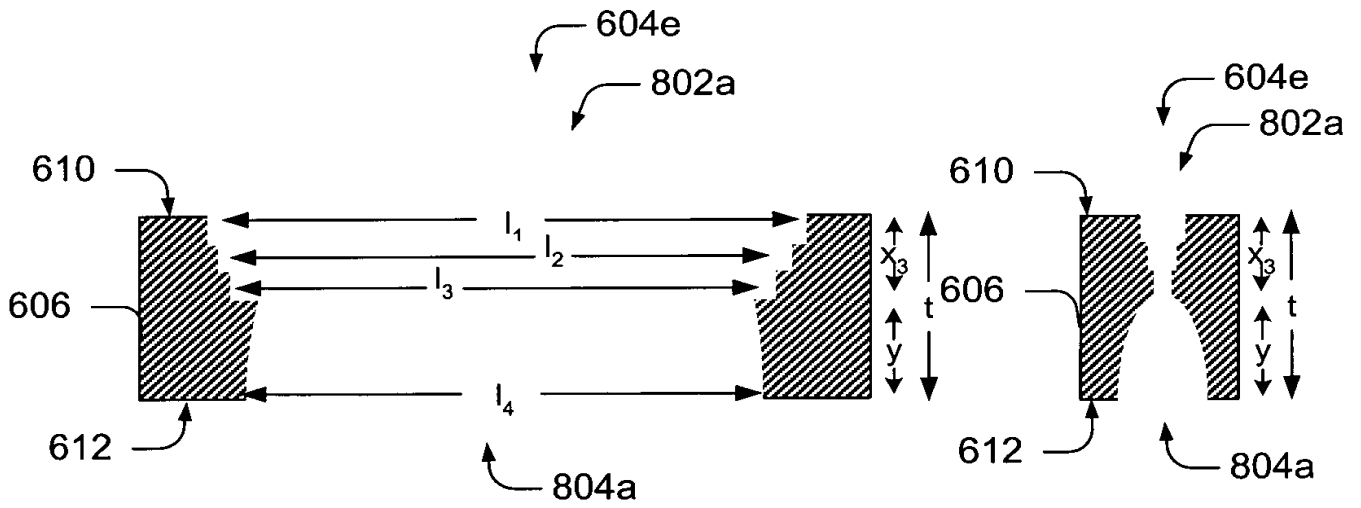


Fig. 9i

Fig. 9j